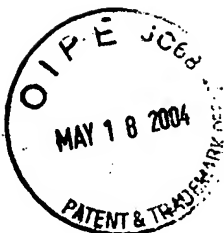


Case No. 62605



IPW

**DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE
WASHINGTON, DC 20231**

In re Application of: **MEARS ET AL.**

Serial No.: **10/603,696**

Filed: **June 26, 2003**

For: **SEMICONDUCTOR STRUCTURES HAVING IMPROVED CONDUCTIVITY
EFFECTIVE MASS AND METHODS FOR FABRICATING SAME**

Sir:

Transmitted herewith is an INFORMATION DISCLOSURE STATEMENT in the above-identified application.


1. ☒ This IDS is submitted under 37 C.F.R. § 1.97. No fee is required.
2. ☐ This IDS is submitted under 37 C.F.R. § 1.97(c). Enclosed is a check in the amount of \$ 180.00.
3. ☐ This IDS is submitted under 37 C.F.R. § 1.97(c) and (e). No fee is required.
4. ☐ This IDS is submitted under 37 C.F.R. § 1.97(d) and (e). Enclosed is a check in the amount of \$ 130.00 to cover the petition fee.
5. ☒ The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.
6. ☐ Please associate this application with Customer No. 27975.



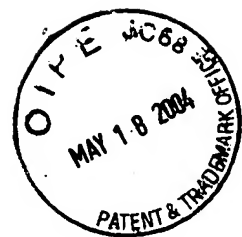
27975

PATENT TRADEMARK OFFICE

Date: May 14, 2004



CHRISTOPHER F. REGAN
Reg. No. 34,906



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
MEARS ET AL.

Serial No. **10/603,696**

Confirmation No. **2901**

Filing Date: **JUNE 26, 2003**

For: **SEMICONDUCTOR STRUCTURES HAVING**
IMPROVED CONDUCTIVITY EFFECTIVE
MASS AND METHODS FOR FABRICATING
SAME

)
)
) Examiner: **Shouxiang HU**

)
) Art Unit: **2811**

)
) Attorney Docket: **62605**

CITATION UNDER 37 CFR §1.97(c)

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a form PTO-1449 listing several references for consideration in the examination of the above-identified application. A copy of each reference is also enclosed. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR §1.97 and Section 609 of the MPEP.

Respectfully submitted,

CHRISTOPHER F. REGAN

Reg. No. 34,906

Allen, Dyer, Doppelt, Milbrath
& Gilchrist, P.A.

255 S. Orange Avenue, Suite 1401
Post Office Box 3791
Orlando, Florida 32802-3791
407-841-2330

In re Patent Application of:

MEARS ET AL.

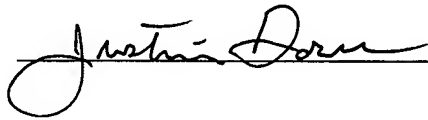
Serial No. **10/603,696**

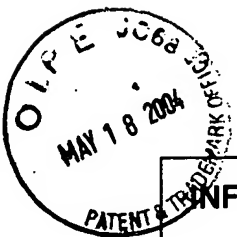
Confirmation No. **2901**

Filed: **June 26, 2003**

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: MAIL STOP AMENDMENT, COMMISSIONER OF PATENTS AND TRADEMARKS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, on this 14th day of May, 2004.



**INFORMATION DISCLOSURE
STATEMENT**

Atty Docket: 62605
Serial No.: 10/603,696
Applicant: MEARS ET AL.
Filing Date: JUNE 26, 2003
Group: 2811

U.S. PATENT DOCUMENTS

Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
	AA	4,594,603	6/10/86	Holonyak, Jr.	357	16	1/24/85
	AB	4,937,204	6/26/90	Ishibashi et al.	437	110	1/4/89
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	AT	2,347,520	09/06/00	GB	G02B	5/18	

EXAMINER:**DATE CONSIDERED:**

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE STATEMENT	Atty Docket:	62605
	Serial No.:	10/603,696
	Applicant:	MEARS ET AL.
	Filing Date:	JUNE 26, 2003
	Group:	2811

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		
	AU	Xuan Luo et al.; "Chemical Design of Direct-Gap Light-Emitting Silicon", published July 25, 2002 by The American Physical Society; Vol. 89, Number 7
	AV	R. Tsu; University of North Carolina at Charlotte, "Phenomena in Silicon Nanostructure Devices"; published 09/06/2000 © Springer-Verlag 2000
	AW	P.D. Ye et al., "GaAs MOSFET with Oxide Gate Dielectric Grown by Atomic Layer Deposition"; © 2003 Agere Systems, March 2003
	AX	Novikov et al.; "Silicon-based Optoelectronics" © 1999-2003 by John Wiley & Sons, Inc.; pp/ 1-6
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